

Embedding Wheel-like Networks

R. Sundara Rajan^a, T. M. Rajalaxmi^b, Sudeep Stephen^{c*},
A. Arul Shantrinal^a, K. Jagadeesh Kumar^a

^aDepartment of Mathematics, Hindustan Institute of Technology and Science,
Chennai, India, 603 103

^bDepartment of Mathematics, Sri Sivasubramaniya Nadar College of
Engineering, Chennai, India, 603 110

^cDepartment of Mathematics, University of Auckland, New Zealand, 1010

E-mail: vprsundar@gmail.com

E-mail: laxmi.raj18@gmail.com

E-mail: sudeep.stephen@auckland.ac.nz

E-mail: shandrinashan@gmail.com

E-mail: jagadeeshgraphs@gmail.com

ABSTRACT. One of the important features of an interconnection network is its ability to efficiently simulate programs or parallel algorithms written for other architectures. Such a simulation problem can be mathematically formulated as a graph embedding problem. In this paper we compute the lower bound for dilation and congestion of embedding onto wheel-like networks. Further, we compute the exact dilation of embedding wheel-like networks into hypertrees, proving that the lower bound obtained is sharp. Again, we compute the exact congestion of embedding windmill graphs into circulant graphs, proving that the lower bound obtained is sharp. Further, we compute the exact wirelength of embedding wheels and fans into 1,2-fault hamiltonian graphs. Using this we estimate the exact wirelength of embedding wheels and fans into circulant graphs, generalized Petersen graphs, augmented cubes, crossed cubes, Möbius cubes, twisted cubes, twisted n -cubes, locally twisted cubes, generalized

*Corresponding Author

twisted cubes, odd-dimensional cube connected cycle, hierarchical cubic networks, alternating group graphs, arrangement graphs, 3-regular planer hamiltonian graphs, star graphs, generalised matching networks, fully connected cubic networks, tori and 1-fault traceable graphs.

Keywords: Embedding, Wheel, Friendship graph, Median, Hamiltonian.

2000 Mathematics subject classification: 05C60, 05C85.

1. INTRODUCTION

Graph embedding is a powerful method in parallel computing that maps a guest network G into a host network H (usually an interconnection network). A graph embedding has a lot of applications, such as processor allocation, architecture simulation, VLSI chip design, data structures and data representations, networks for parallel computer systems, biological models that deal with visual stimuli, cloning and so on [1, 2, 3, 4].

The performance of an embedding can be evaluated by certain cost criteria, namely the dilation, the edge congestion and the wirelength. The *dilation* of an embedding is defined as the maximum distance between pairs of vertices of the host graph that are images of adjacent vertices of the guest graph. It is a measure for the communication time needed when simulating one network on another. The *congestion* of an embedding is the maximum number of edges of the guest graph that are embedded on any single edge of the host graph. An embedding with a large congestion faces many problems, such as long communication delay, circuit switching and the existence of different types of uncontrolled noise. The *wirelength* of an embedding is the sum of the dilations in host graph of edges in guest graph [3, 5].

Ring or path embedding in interconnection networks is closely related to the hamiltonian problem [6–9] which is one of the well known NP-complete problems in graph theory. If an interconnection network has a hamiltonian cycle or a hamiltonian path, ring or linear array can be embedded in this network. Embedding of linear arrays and rings into a faulty interconnection network is one of the central issues in parallel processing. The problem is modeled as finding fault-free paths and cycles of maximum length in the graph [10].

The wheel-like networks plays an important role in the circuit layout and interconnection network designs. Embedding of wheels and fans in interconnection networks is closely related to 1-fault hamiltonian problem. A graph G is called f -fault hamiltonian if there is a cycle which contains all the non-faulty vertices and contains only non-faulty edges when there are f or less faulty vertices and/or edges. Similarly, a graph G is called f -fault traceable if for each pair of vertices u and v , there is a path from u to v which contains all the

non-faulty vertices and contains only non-faulty edges when there are f or less faulty vertices and/or edges. We note that if a graph G is hypohamiltonian, hyperhamiltonian or almost pancyclic then it is 1-fault hamiltonian [11] and it has been well studied in [8, 11, 12].

The rest of the paper is organized as follows: Section 2 gives definitions and other preliminaries. In Section 3, we compute the dilation, congestion and wirelength of embedding onto wheel-like networks. Finally, concluding remarks and future works are given in Section 4.

2. PRELIMINARIES

In this section we give basic definitions and preliminaries related to embedding problems.

Definition 2.1. [13] Let G and H be finite graphs. An *embedding* of G into H is a pair (f, P_f) defined as follows:

- (1) f is a one-to-one map: $V(G) \rightarrow V(H)$
- (2) P_f is a one-to-one map from $E(G)$ to $\{P_f(e) : P_f(e) \text{ is a path in } H \text{ between } f(u) \text{ and } f(v) \text{ for } e = uv \in E(G)\}$.

By abuse of language we will also refer to an embedding (f, P_f) simply by f . The *expansion* of an embedding f is the ratio of the number of vertices of H to the number of vertices of G . In this paper, we consider embeddings with expansion one.

Definition 2.2. [13] Let f be an embedding of G into H . If $e = uv \in E(G)$, then the length of $P_f(e)$ in H is called the *dilation* of the edge e denoted by $dil_f(e)$. Then

$$dil(G, H) = \min_{f:G \rightarrow H} \max_{e \in E(G)} dil_f(e).$$

Definition 2.3. [13] Let f be an embedding of G into H . For $e \in E(H)$, let $EC_f(e)$ denotes the number of edges xy of G such that e is in the path $P_f(xy)$ between $f(x)$ and $f(y)$ in H .

In other words, $EC_f(e) = |\{xy \in E(G) : e \in P_f(xy)\}|$. Then

$$EC(G, H) = \min_{f:G \rightarrow H} \max_{e \in E(H)} EC_f(e).$$

Further, if S is any subset of $E(H)$, then we define $EC_f(S) = \sum_{e \in S} EC_f(e)$.

Definition 2.4. [14] Let f be an embedding of G into H . Then the wirelength of embedding G into H is given by

$$WL(G, H) = \min_{f:G \rightarrow H} \sum_{e \in E(G)} dil_f(e) = \min_{f:G \rightarrow H} \sum_{e \in E(H)} EC_f(e).$$

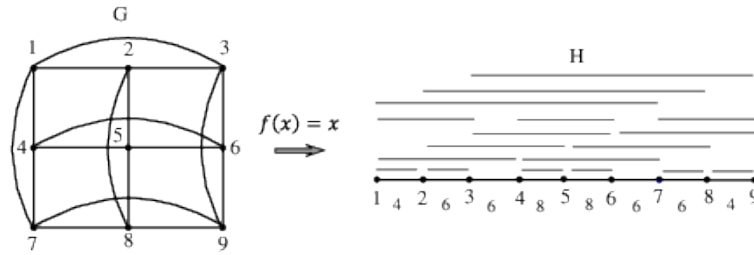


FIGURE 1. Wiring diagram of torus G into path H with $dil_f(G, H) = 6$, $EC_f(G, H) = 8$ and $WL_f(G, H) = 48$.

An illustration for dilation, congestion and wirelength of an embedding torus G into a path H is given in Fig. 1. The dilation, the congestion, and the wirelength problem are different in the sense that an embedding that gives the minimum dilation need not give the minimum congestion (wirelength) and vice-versa. But, it is interesting to note that, for any embedding g , the dilation sum, the congestion sum and the wirelength are all equal.

Graph embeddings have been well studied for a number of networks [1,2, 4–7, 11, 13–34]. Even though there are numerous results and discussions on the wirelength problem, most of them deal with only approximate results and the estimation of lower bounds [13, 18]. But the Congestion Lemma and the Partition Lemma [14] have enabled the computation of exact wirelength for embeddings of various architectures [14, 21, 23, 24, 32, 33]. In fact, the techniques deal with the congestion sum [14] to compute the exact wirelength of graph embeddings. In this paper, we overcome this difficulty by taking non-regular graphs as guest graphs and use dilation-sum to find the exact wirelength.

Definition 2.5. [19, 35] A wheel graph W_n of order n is a graph that contains an outer cycle or rim of order $n - 1$, and for which every vertex in the cycle is connected to one other vertex (which is known as the hub or center). The edges of a wheel which include the hub are called spokes.

Definition 2.6. [11, 36] A fan graph F_n of order n is a graph that contains a path of order $n - 1$, and for which every vertex in the path is connected to one other vertex (which is known as the core). In other words, a fan graph F_n is obtained from W_n by deleting any one of the outer cycle edges.

Definition 2.7. [36] A friendship graph T_n of order $2n + 1$ is a graph consists of n triangles with exactly one common vertex called the hub or center. Alternatively, a friendship graph T_n can be constructed from a wheel W_{2n+1} by removing every second outer cycle edge.

Definition 2.8. A windmill graph WM_n of order $2n$ is obtained by deleting a vertex v of degree 2 in T_n .

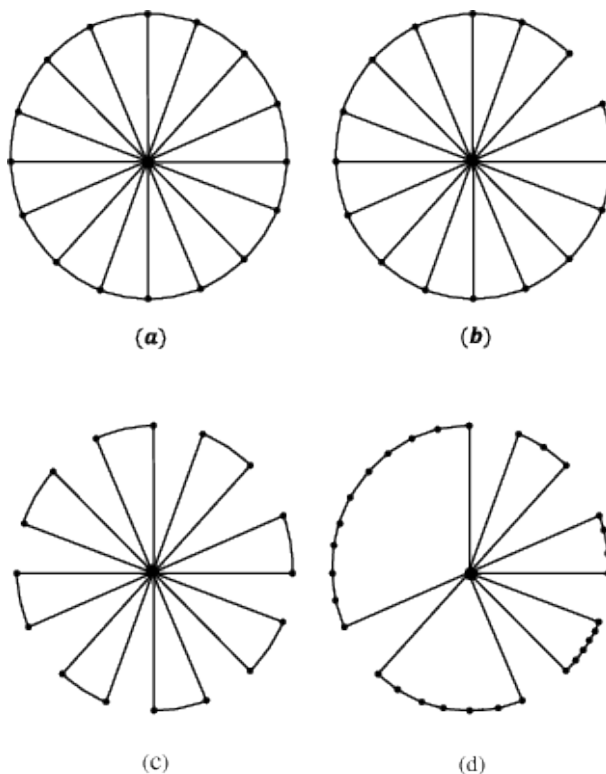


FIGURE 2. (a) Wheel graph W_{17} (b) Fan graph F_{17} (c) Friendship graph T_8 and (d) Windmill graph WM_8 .

Definition 2.9. [3] A star graph S_n is the complete bipartite graph $K_{1,n-1}$.

Figures 2(a), 2(b), 2(c) and 2(d) illustrate the wheel graph W_{12} , fan F_{12} , friendship graph T_8 and windmill graph WM_8 respectively.

Definition 2.10. [37] The basic skeleton of a hypertree is a complete binary tree T_r , where r is the level of a tree. Here the nodes of the tree are numbered as follows: The root node has label 1. The root is said to be at level 1. Labels of left and right children are formed by appending a 0 and 1, respectively, to the label of the parent node, see Fig. 3(a). The decimal labels of the hypertree in Fig. 3(a) are depicted in Fig. 3(b). Here the children of the node x are labeled as $2x$ and $2x + 1$. Additional links in a hypertree are horizontal and two nodes in the same level i of the tree are joined if their label difference is 2^{i-2} . We denote an r level hypertree as $HT(r)$. It has $2^r - 1$ vertices and $3(2^{r-1} - 1)$ edges.

Definition 2.11. [34] For any non-negative integer r , the complete binary tree of height $r - 1$, denoted by T_r , is the binary tree where each internal vertex

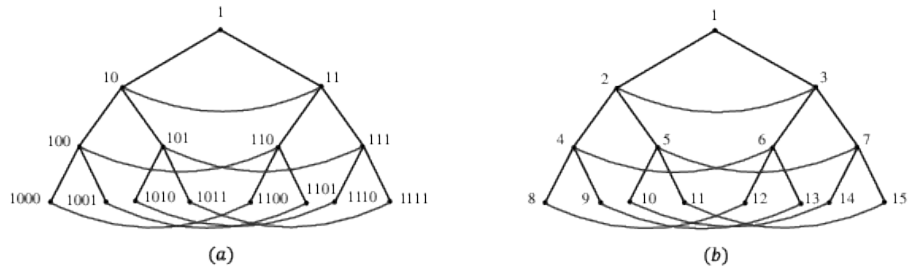


FIGURE 3. (a) $HT(4)$ with binary labels (b) $HT(4)$ with decimal labels.

has exactly two children and all the leaves are at the same level. Clearly, a complete binary tree T_r has r levels. Each level i , $1 \leq i \leq r$, contains 2^{i-1} vertices. Thus, T_r has exactly $2^r - 1$ vertices. The sibling tree ST_r is obtained from the complete binary tree T_r by adding edges (sibling edges) between left and right children of the same parent node.

Definition 2.12. The X -tree XT_r is obtained from the complete binary tree T_r by adding the consequent vertices in each level by an edge.

For illustration, the sibling tree $ST(5)$ and X -tree XT_5 are given in Figure 4.

Definition 2.13. [22, 38] The undirected circulant graph $G(n; \pm S)$, $S \subseteq \{1, 2, \dots, j\}$, $1 \leq j \leq \lfloor n/2 \rfloor$, is a graph with the vertex set $V = \{0, 1, \dots, n-1\}$ and the edge set $E = \{ik : |k - i| \equiv s \pmod n, s \in S\}$.

It is clear that $G(n; \pm 1)$ is the undirected cycle C_n and $G(n; \pm\{1, 2, \dots, \lfloor n/2 \rfloor\})$ is the complete graph K_n . The cycle $G(n; \pm 1) \simeq C_n$ contained in $G(n; \pm\{1, 2, \dots, j\})$, $1 \leq j \leq \lfloor n/2 \rfloor$ is sometimes referred to as the outer cycle C of G .

Definition 2.14. [19] Let v be a vertex in G . The eccentricity of v , denoted by $\epsilon(v)$, is $\epsilon(v) = \max\{d(u, v) | u \in V\}$. The maximum eccentricity is the graph diameter $d(G)$. That is, $d(G) = \max\{\epsilon(v) : v \in V\}$. The minimum eccentricity is the graph radius $r(G)$. That is, $r(G) = \min\{\epsilon(v) : v \in V\}$. For brevity, we denote $d(G)$ and $r(G)$ as d and r respectively.

Notation: For a graph G , the minimum degree and the maximum degree is denoted by $\delta(G)$ and $\Delta(G)$ respectively. For $u \in V(G)$, let $N_i(u)$ denotes the set of all vertices of G at distance i from u , $1 \leq i \leq d$, where d denotes the diameter of G .

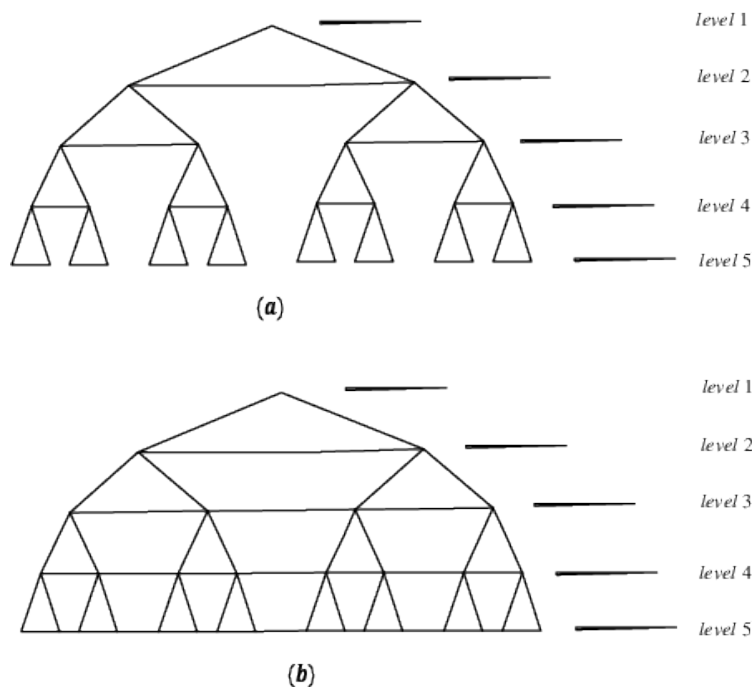


FIGURE 4. (a) Sibling tree $ST(5)$ (b) X -tree XT_5 .

3. MAIN RESULTS

In this section we compute the dilation, congestion and wirelength of embedding onto wheel-like networks.

3.1. Dilation.

Lemma 3.1. *Let G be a graph with $\Delta(G) = n - 1$ and H be a graph with $|V(G)| = |V(H)| = n$. Then $dil(G, H) \geq r$, where r is the radius of H .*

Proof. Since $\Delta(G) = n - 1$, there exists a vertex $u \in V(G)$ such that $d(u) = n - 1$. Let f be an embedding from $V(G)$ to $V(H)$ and map $f(u) = v$. If eccentricity of v is minimum, then $dil(G, H) \geq r$. Otherwise, $dil(G, H) \geq r + 1$. Hence the proof. \square

Corollary 3.2. *Let G be a graph with $\Delta(G) = n - 1$ and H be a vertex-transitive graph with $|V(G)| = |V(H)| = n$. Then $dil(G, H) = d$, where d is the diameter of H .*

We now compute the dilation of embedding wheel-like networks into hyper-tree and prove that the lower bound obtained in Lemma 3.1 is sharp.

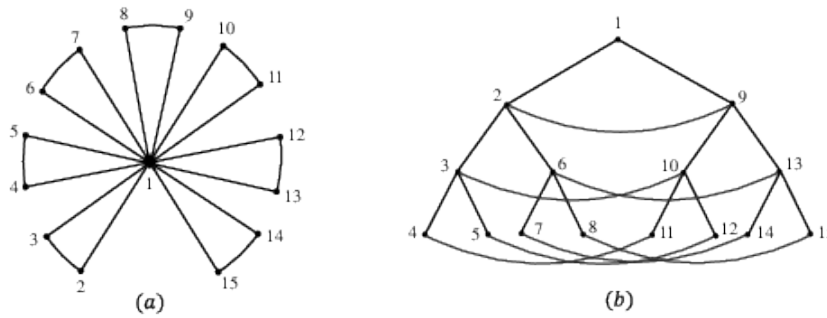


FIGURE 5. (a) Labelling of T_7 (b) Labelling of $HT(4)$.

Theorem 3.3. *Let G be W_n or F_n or $T_{\frac{n-1}{2}}$ or S_n , and H be an l -level hypertree $HT(l)$, where $2^l - 1 = n$, $l \geq 3$. Then $dil(G, H) = r = l - 1$, where r is the radius of H .*

Proof. Since $\Delta(G) = n - 1$ and by Lemma 3.1, we have $dil(G, H) \geq r$. We now prove the equality.

Label the vertices of G as follows:

- hub vertex as 1;
- outer vertices as $2, 3, \dots, n$ consecutively start with any vertex in the clockwise or anti-clockwise direction, see Fig. 5(a).

Removal of the horizontal edges in hypertree $HT(l)$ leaves a complete binary tree T_l . Label the vertices of T_l using pre-order labeling begin with level 1 vertex, see Fig. 5(b). Let $f(x) = x$ for all $x \in V(G)$, and for $ab \in E(G)$ let $P_f(ab)$ be a shortest path between $f(a)$ and $f(b)$ in $HT(l)$.

Since the hub vertex with label 1 in $V(G)$ is mapped into a vertex $f(1) = 1$ in $V(H)$ is in level 1 gives the minimum eccentricity of H and hence any edge $e = uv \in E(G)$ with either u or v as a hub vertex is mapped into a path $P_f(uv)$ in H with dilation at most $l - 1$, which is nothing but the radius r of H .

We now claim that the outer edges of G are mapped into a path of length at most $l - 1$ in H . Since the graph H is obtained from T_l , the left and right children of any parent node in level $l - 1$ is connected by a path of length 2. By the labeling of pre-order traversal in T_l , for any parent node in level i , $1 \leq i \leq l - 2$, the right most vertex of a left node and the right node of a parent node are connected by a path length at most $l - 1$ and hence the dilation of any outer edge in G is at most $l - 1$ in H . Hence the proof. \square

Using the same approach, we prove the following result.

Theorem 3.4. *Let G be W_n or F_n or $T_{\frac{n-1}{2}}$ or S_n and H be a l -level sibling tree $ST(l)$ or l -level X -tree XT_l , where $2^l - 1 = n$, $l \geq 3$. Then $dil(G, H) = r = l - 1$, where r is the radius of H .*

3.2 Congestion.

In this section, we first obtain the lower bound for congestion of embedding onto wheel-like networks. Then prove that the lower bound obtained is sharp for embedding windmill graphs into circulant graphs. To prove the main result, we need the following result.

Lemma 3.5. *Let G be a graph with $\Delta(G) = n - 1$ and H be a graph with $|V(G)| = |V(H)| = n$. Then $EC(G, H) \geq \lceil \frac{n-1}{\Delta(H)} \rceil$.*

Proof. Since $\Delta(G) = n - 1$, there exist a vertex $u \in V(G)$ such that $d(u) = n - 1$, where $n = |V(G)|$. Let f be an embedding from $V(G)$ to $V(H)$ and map $f(u) = v$. Let $S = \{e : d(v, w) = 1, w \in V(H)\}$, then for any $e \in S$,

$$EC_f(e) \geq \min \left\{ \frac{n-1}{\delta_0}, \frac{n-1}{\delta_1}, \dots, \frac{n-1}{\delta_n} \right\} = \left\lceil \frac{n-1}{\delta_n} \right\rceil,$$

where δ_i is the degree of a vertex v_i in H with $\delta(H) = \delta_0 \leq \delta_1 \leq \dots \leq \delta_i \leq \dots \leq \delta_n = \Delta(H)$, $0 \leq i \leq n$. Thus, there is at least one edge in H with congestion $\left\lceil \frac{n-1}{\Delta(H)} \right\rceil$. Further, for any embedding g of G into H , $EC_g(e) \geq EC_f(e) \geq \left\lceil \frac{n-1}{\Delta(H)} \right\rceil$. Therefore,

$$EC(G, H) \geq \min_g EC_g(e) \geq \min_g EC_f(e) \geq \left\lceil \frac{n-1}{\Delta(H)} \right\rceil.$$

Hence the proof. □

We now compute the edge congestion of embedding windmill graphs into circulant networks and prove that the lower bound obtained in Lemma 3.5 is sharp.

Theorem 3.6. *Let G be a windmill graph WM_{2^n-1} and H be a circulant network $H(2^n; \pm\{1, 2^{n-2}\})$, $n \geq 3$. Then $EC(G, H) = 2^{n-2}$.*

Proof. Since $\Delta(G) = n - 1$ and by Lemma 3.5, $EC(G, H) \geq 2^{n-2}$. We now prove the equality.

Label the vertices of G as follows:

- hub vertex as 1;
- pendent vertex as 2^n ;
- remaining vertices as $2, 3, \dots, 2^n - 1$ consecutively start with any vertex such that $(i, i + 1)$ are adjacent, where i even and $2 \leq i \leq 2^n - 2$.

Label the consecutive vertices of $H(2^n; \pm\{1\})$ in H in the clockwise sense. Let $f(x) = x$ for all $x \in V(G)$ and for $ab \in E(G)$, let $P_f(ab)$ be a shortest path between $f(a)$ and $f(b)$ in H .

Since H is vertex transitive, map the hub vertex u , which is labeled as 1 in G into any vertex $v = f(u)$ in H . Without loss of generality, the label of v as 1 i.e., $f(u) = f(1) = 1 = v$. Now, we map the edges in G into a path P_f in H using the following algorithm.

- For $(1i) \in E(G)$, let $P_f(1i)$ pass through the outer cycle of H in the clockwise direction, where $2 \leq i \leq 2^{n-2} + 1$;
- For $(1i) \in E(G)$, let $P_f(1i)$ pass through the outer cycle of H in the anti-clockwise direction, where $3 \cdot 2^{n-2} + 1 \leq i \leq 2^n$;
- For $(1i) \in E(G)$, let $P_f(1i)$ pass through an edge, which is labelled as $(1, 2^{n-2} + 1)$ followed by the outer cycle of H in the clockwise direction, where $2^{n-2} + 2 \leq i \leq 2^{n-1} + 1$;
- For $(1i) \in E(G)$, let $P_f(1i)$ pass through an edge, which is labelled as $(1, 3 \cdot 2^{n-2} + 1)$ followed by the outer cycle of H in the anti-clockwise direction, where $2^{n-1} + 2 \leq i \leq 3 \cdot 2^{n-2}$.

From the above algorithm, it is easy to see that the edge congestion of each edge in H is at most 2^{n-2} . At this stage, the following edges in H have 2^{n-2} as the edge congestion and we denote the set by $A = \{(1, 2), (1, 2^{n-2} + 1), (2^{n-2} + 1, 2^{n-2} + 2), (1, 2^n)\}$. Now, the remaining edges $(i, i+1)$, $2 \leq i \leq 2^n - 2$ and i is even in $E(G)$ is mapped into a path of length 1 in H and it will not contribute the congestion in any of the edges in A . Hence the proof. \square

3.3 Wirelength

First, we start with the following definitions.

Definition 3.7. A graph G is hamiltonian if it has a hamiltonian cycle. A hamiltonian graph G is k -fault hamiltonian if $G - F$ remains hamiltonian for every $F \subset V(G)$ with $|F| \leq k$.

Definition 3.8. For every $v \in V$ in G , define $D(v) = \sum_{u \in V} d(u, v)$, where $d(u, v)$ is the distance between u and v in G . A vertex v for which $D(v)$ is minimum is called a median of G .

Theorem 3.9. Let G be a wheel graph W_n and H be a graph with u as a median. Then $WL(G, H) \geq n - 1 + D(u)$. Equality holds if and only if $H \setminus u$ is hamiltonian.

Proof. Let u be the hub of W_n . Map u in G to u in H . Since u is a median of H , $D(u) = \sum_{v \in V} d(u, v) = \sum_{i=1}^k |N_i(u)|$, $k \leq d$. Suppose $H \setminus u$ is hamiltonian. Map the outer $(n - 1)$ -cycle in G to a hamiltonian cycle in $H \setminus u$. Thus

$$WL(G, H) = n - 1 + \sum_{i=1}^k |N_i(u)|, k \leq d.$$

Conversely, suppose $WL(G, H) = n - 1 + D(u)$. If $H \setminus u$ is not hamiltonian, then the cycle in G cannot be mapped onto a cycle in $H \setminus u$, a contradiction. \square

Proceeding in the same way, we have the following result.

Theorem 3.10. *Let G be a fan graph F_n and H be a graph with u as a median. Then $WL(G, H) \geq n - 2 + D(u)$. Equality holds if and only if $H \setminus u$ contains a hamiltonian path.*

The host graphs in Theorem 3.9 and Theorem 3.10 cover a wide range of graphs. This has motivate us to identify interconnection networks which fall into this category:

Networks	Justification for 1-fault Tolerance
Circulant graphs $G(n; \pm S), \{1, 2\} \subseteq S \subseteq \{1, 2, \dots, \lfloor \frac{n}{2} \rfloor\}$	1-fault hamiltonian [3]
Generalized Petersen graphs $P(n, m)$	hypohamiltonian/hyperhamiltonian [3, 39]
Augmented cubes AQ_n	pancyclic [40]
Crossed cubes CQ_n	almost pancyclic [41]
Möbius cubes MQ_n	$(n - 2)$ -fault almost pancyclic [10, 42]
Twisted cubes TQ_n	$(n - 2)$ -fault almost pancyclic [10, 43, 44]
Twisted n -cubes T_nQ	1-fault hamiltonian [45]
Locally twisted cubes LTQ_n	almost pancyclic [46]
Generalized twisted cubes GQ_n	$(n - 2)$ -fault almost pancyclic [10]
Odd dimensional cube connected cycle CCC_n	1-fault hamiltonian [46]
Hierarchical cubic networks $HCN(n)$	almost pancyclic [47]
Alternating group graphs AG_n	$(n - 2)$ -fault hamiltonian [48]
Arrangement graphs $A_{n,k}$	pancyclic [49]
3-regular planar hamiltonian graphs	1-fault hamiltonian [50]
(n, k) -star graphs $S_{n,k}$	at most $(n - 3)$ -fault hamiltonian [51]
Generalised matching network GMN	$(f + 2)$ -fault hamiltonian [52]
Fully connected cubic networks $FCCN_n$	1-fault hamiltonian [53]
Tori $T(d_1, d_2, \dots, d_n)$	fault hamiltonian [54, 55]
1-fault traceable graphs	2-fault hamiltonian [Definition 3.7]

TABLE 1. List of 1-fault hamiltonian networks.

ACKNOWLEDGMENTS

The work of R. Sundara Rajan is partially supported by Project no. ECR/2016/1993, Science and Engineering Research Board (SERB), Department of Science and Technology (DST), Government of India. The work of Sudeep Stephen has been done when he was a postdoc in National University of Singapore, Singapore. Further, we thank Prof. N. Parthiban, School of Computing Sciences and Engineering, SRM Institute of Science and Technology, Chennai, India for his fruitful suggestions. In addition, the authors would like to thank the anonymous referees for their comments and suggestions. These comments and suggestions were very helpful for improving the quality of this paper.

REFERENCES

1. J. Fan, X. Lin, X. Jia, Optimal Path Embedding in Crossed Cubes, *IEEE Transactions on Parallel and Distributed Systems*, **16**(12), (2005), 1190-1200.
2. C. N. Kuo, Y. H. Cheng, Cycles Embedding in Folded Hypercubes with Conditionally Faulty Vertices, *Discrete Applied Mathematics*, **220**, (2017), 55-59.
3. J. M. Xu, *Topological Structure and Analysis of Interconnection Networks*, Network theory and applications, Springer Science & Business Media, **7**, 2013.
4. R. S. Rajan, T. M. Rajalaxmi, J. B. Liu, G. Sethuraman, Wirelength of Embedding Complete Multipartite Graphs into Certain Graphs, *Discrete Applied Mathematics*, **280**, (2020), 221-236.
5. Y. L. Lai, K. Williams, A Survey of Solved Problems and Applications on Bandwidth, Edgesum, and Profile of Graphs, *Journal of Graph Theory*, **31**, (1999), 75-94.
6. T. J. Lin, S. Y. Hsieh, J. S. T. Juan, Embedding Cycles and Paths in Product Networks and their Applications to Multiprocessor Systems, *IEEE Transactions on Parallel and Distributed Systems*, **23**(6), (2012), 1081-1089.
7. M. Liu, H. Liu, Paths and Cycles Embedding on Faulty Enhanced Hypercube Networks, *Acta Mathematica Scientia*, **33**(1), (2013), 227-246.
8. C. Chen, C. H. Tsai, L. H. Hsu, J. M. Tan, Pn Some Super Fault-tolerant Hamiltonian Graphs, *Applied Mathematics and Computation*, **148**(3), (2004), 729-741.
9. M. R. Garey, D.S. Johnson, *Computers and Intractability*, A Guide to the Theory of NP-Completeness, Freeman, San Francisco, 1979.
10. J. H. Park, H. S. Lim, H. C. Kim, Panconnectivity and Pancyclicity of Hypercube-like Interconnection Networks with Faulty Elements, *Theoretical Computer Science*, **377**(1-3), (2007), 170-180.
11. M. Arockiaraj, P. Manuel, I. Rajasingh, B. Rajan, Wirelength of 1-fault Hamiltonian Graphs into Wheels and Fans, *Information Processing Letters*, **111**, (2011), 921-925.
12. C. H. Tsai, T. K. Li, Two Construction Schemes for Cubic Hamiltonian 1-node-hamiltonian Graphs, *Mathematical and Computer Modelling*, **48**(3-4), (2008), 656-661.
13. S. L. Bezrukov, J. D. Chavez, L. H. Harper, M. Röttger, U. P. Schroeder, Embedding of Hypercubes into Grids, *MFCS*, (1998), 693-701.
14. P. Manuel, I. Rajasingh, B. Rajan, H. Mercy, Exact Wirelength of Hypercube on a Grid, *Discrete Applied Mathematics*, **157**(7), (2009), 1486-1495.
15. J. M. Xu, M. Ma, Survey on Path and Cycle Embedding in Some Networks, *Frontiers of Mathematics in China*, **4**, (2009), 217-252.

16. S. L. Bezrukov, J. D. Chavez, L. H. Harper, M. Röttger, U. P. Schroeder, The Congestion of n -cube Layout on a Rectangular Grid, *Discrete Mathematics*, **213**, (2000), 13–19.
17. J. Opatrny, D. Sotteau, Embeddings of Complete Binary Trees into Grids and Extended Grids with Total Vertex-congestion 1, *Discrete Applied Mathematics*, **98**, (2000), 237–254.
18. J. D. Chavez, R. Trapp, The Cyclic Cutwidth of Trees, *Discrete Applied Mathematics*, **87**, (1998), 25–32.
19. I. Rajasingh, J. Quadras, P. Ma, A. William, Embedding of Cycles and Wheels into Arbitrary Trees, *Networks*, **44**, (2004), 173–178.
20. W. K. Chen, M. F. M. Stallmann, On Embedding Binary Trees into Hypercubes, *Journal on Parallel and Distributed Computing*, **24**, (1995), 132 - 138.
21. P. Manuel, M. Arockiaraj, I. Rajasingh, B. Rajan, Embedding Hypercubes into Cylinders, Snakes and Caterpillars for Minimizing Wirelength, *Discrete Applied Mathematics*, **159**(17), (2011), 2109–2116.
22. I. Rajasingh, B. Rajan, R. S. Rajan, Embedding of Special Classes of Circulant Networks, Hypercubes and Generalized Petersen Graphs, *International Journal of Computer Mathematics*, **89**(15), (2012), 1970–1978.
23. J. Fan, X. Jia, Embedding Meshes into Crossed Cubes, *Information Sciences*, **177**(15), (2007), 3151–3160.
24. Y. Han, J. Fan, S. Zhang, J. Yang, P. Qian, Embedding Meshes into Locally Twisted Cubes, *Information Sciences*, **180**(19), (2010), 3794–3805.
25. X. Yang, Q. Dong, Y. Y. Tan, Embedding Meshes/tori in Faulty Crossed Cubes, *Information Processing Letters*, **110**(14-15), (2010), 559–564.
26. R. Caha, V. Koubek, Optimal Embeddings of Ladders into Hypercubes, *Discrete Mathematics*, **233**, (2001), 65–83.
27. B. Chen, On Embedding Rectangular Grids in Hypercubes, *IEEE Transactions on Computers*, **37**(10), (1988), 1285–1288.
28. J. A. Ellis, Embedding Rectangular Grids into Square Grids, *IEEE Transactions on Computers*, **40**(1), (1991), 46–52.
29. M. Rottger, U. P. Schroeder, Efficient Embeddings of Grids into Grids, *Discrete Applied Mathematics*, **108**(1-2), (2001), 143–173.
30. C. -H. Tsai, Embedding of Meshes in Möbius Cubes, *Theoretical Computer Science*, **401**(1-3), (2008), 181–190.
31. P. -L. Lai, C. -H. Tsai, Embedding of Tori and Grids into Twisted Cubes, *Theoretical Computer Science*, **411**(40-42), (2010), 3763–3773.
32. I. Rajasingh, M. Arockiaraj, B. Rajan, P. Manuel, Minimum Wirelength of Hypercubes into n -dimensional Grid Networks, *Information Processing Letters*, **112**, (2012), 583–586.
33. I. Rajasingh, B. Rajan, R. S. Rajan, Embedding of Hypercubes into Necklace, Windmill and Snake Graphs, *Information Processing Letters*, **112**, (2012), 509–515.
34. I. Rajasingh, P. Manuel, B. Rajan, M. Arockiaraj, Wirelength of Hypercubes into Certain Trees, *Discrete Applied Mathematics*, **160**, (2012), 2778 - 2786.
35. B. R. Myers, Number of Spanning Trees in a Wheel, *IEEE Transactions on Circuit Theory*, **18**, (1971), 280–282.
36. Slammin, M. Bača, Y. Lin, M. Miller, R. Simanjuntak, Edge-magic Total Labelings of Wheels, Fans and Friendship Graphs, *Bulletin of the Institute of Combinatorics and its Applications*, **35**, (2002), 89–98.
37. J. R. Goodman, C. H. Sequin, A Multiprocessor Interconnection Topology, *IEEE Transactions on Computers*, **c-30**(12), (1981), 923–933.
38. J. C. Bermond, F. Comellas, D. F. Hsu, Distributed Loop Computer Networks, *A survey: Journal of Parallel and Distributed Computing*, **24**(1), (1995), 2-10.

39. T. C. Mai, J. J. Wang, L. -H. Hsu, Hyperhamiltonian Generalized Petersen Graphs, *Computer and Mathematics with Applications*, **55**(9), (2008), 2076–2085.
40. S. A. Choudum and V. Sunitha, Augmented Cubes, *Networks*, **40**(2), (2002), 71–84.
41. E. Efe, A Variation on the Hypercube with Lower Diameter, *IEEE Transactions on Computers*, **40**(11), (1991), 1312–1316.
42. P. Cull, S. M. Larson, The Möbius Cubes, *IEEE Transactions on Computers*, **44**(5), (1995), 647–659.
43. J. Fan, X. Jia, X. Lin, Optimal Embeddings of Paths with Various Lengths in Twisted Cubes, *IEEE Transactions on Parallel and Distributed Systems*, **18**(4), (2007), 511–521.
44. J. Fan, X. Jia, X. Lin, Embedding of Cycles in Twisted Cubes with Edge Pancyclic, *Algorithmica*, **51**(3), (2008), 264–282.
45. J. -H. Park, H. -C. Kim, H. -S. Lim, *Fault-hamiltonicity of Hypercube-like Interconnection Networks*, in: Proc. of IEEE International Parallel and Distributed Processing Symposium, IPDPS 2005, Denver, 2005.
46. X. Yang, G. M. Megson, D. J. Evans, Locally Twisted Cubes are 4-pancyclic, *Applied Mathematics Letters*, **17**, (2004), 919–925.
47. K. Ghose, K. R. Desai, Hierarchical Cubic Networks, *IEEE Transactions on Parallel and Distributed Systems*, **6**(4), (1995), 427–435.
48. J. -M. Chang, J. -S. Yang, Y. -L. Wang, Y. Cheng, Panconnectivity, Fault Tolerant Hamiltonicity and Hamiltonian-connectivity in Alternating Group Graphs, *Networks*, **44**, (2004), 302–310.
49. K. Day, A. Tripathi, Arrangement Graphs: A Class of Generalized Star Graphs, *Information Processing Letters*, **42**(5), (1992), 235–241.
50. J. J. Wang, C. N. Hung, L. H. Hsu, Optimal 1-hamiltonian Graphs, *Information Processing Letters*, **65**(3), (1998), 157–161.
51. H. C. Hsu, Y. L. Hsieh, J. J. M. Tan, L.-H. Hsu, Fault Hamiltonicity and Fault Hamiltonian Connectivity of the (n, k) -star Graphs, *Networks*, **42**(4), (2003), 189–201.
52. Q. Donga, X. Yanga, J. Zhaob, Fault Hamiltonicity and Fault Hamiltonian-connectivity of Generalised Matching Networks, *International Journal of Parallel, Emergent and Distributed Systems*, **24**(5), (2009), 455–461.
53. T. Y. Ho, C. K. Lin, Fault-tolerant Hamiltonian Connectivity and Fault-tolerant Hamiltonicity of the Fully Connected Cubic Networks, *Journal of Information Science and Engineering*, **25**, (2009), 1855–1862.
54. H. C. Kim, J. H. Park, Fault Hamiltonicity of Two-dimensional Torus Networks, in: *Proc. Japan-Korea Joint Workshop on Algorithms and Computation*, (2000), 110–117.
55. H. -C. Kim, J. -H. Park, Paths and Cycles in d-dimensional Tori with Faults, in: *Workshop on Algorithms and Computation WAAC01, Pusan, Korea*, (2001), 67–74.